



Dr. Ilsup Jin

SK Hynix, Korea



Ilsup Jin is a senior vice president and a head of DRAM/NAND TD in R&D center at SK Hynix. He is responsible for the research and development of next generation DRAM/NAND and future memory technology roadmap. He joined SK Hynix in 2018.

Prior to his current role, Jin led New Architecture Research group in R&D center for the new memory R&D and DRAM PI group in DRAM product division for developing premium DRAM products to transfer to HVM.

Before joining SK Hynix, Jin started his semiconductor career as a process integration engineer at Intel and had technical leadership roles in 32, 14, 10, 7nm node development projects.

Jin earned his Ph.D. degree in materials science and engineering from University of California, Los Angeles.