Prof. Won-Jun Lee

Sejong University, Republic of Korea

Won-Jun Lee has been a professor in the Department of Nanotechnology and Advanced Materials Engineering at Sejong University in Seoul, Republic of Korea, since 2001. His research interests include materials, processes, and equipment for next-generation semiconductor device applications. Currently, he is investigating the surface reaction mechanisms of atomic layer deposition and etching (ALD and ALE) through *in situ* characterization and density functional theory (DFT) calculations. In 2015, he was a visiting scientist at Grenoble INP, France. From 2007 to 2012, he served as the vice director of the Consortium of Semiconductor Advanced Research (COSAR) of Korea.

Prior to joining Sejong University, he worked at LG Semicon and Hyundai Electronics (now SK hynix) from 1996 to 2001. He worked on aluminum interconnects for 0.25 and 0.18 μm foundry processes and a CVD-PVD integrated aluminum plug process for 0.10 μm DRAM devices. From 1996 to 1997, he was a visiting scholar at Stanford University, where he evaluated low-k materials for copper interconnects.

He received his B.S., M.S., and Ph.D. in materials science and engineering from the Korea Advanced Institute of Science and Technology (KAIST) in 1991, 1993, and 1996, respectively.