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Dr. Chung is a VP of Technology in the Process Development at Samsung Electronics. He received the Ph.D. degree in the department of Physics from Yonsei University in 2006 for his study on initial atomic reaction at silicon surface using Scanning Tunneling Microscopy. Since joining Samsung Electronics' Semiconductor R&D Center in 2006, he has been involved in working on the next-generation high-k atomic layer deposition & treatment processes, equipment & materials for transistor and capacitor modules of memory devices.

He is currently leading the R&D of various thin film deposition and treatment processes for DRAM devices. His current research interests mainly focus not only on new materials & fabrication techniques for advanced transistor modules, but also on electrical characterization and modeling related to device performance and reliability.