



## **CMP, Master, Hoyoung Kim**

### **Process Development**

### **Samsung Electronics**

(Republic of Korea)



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Hoyoung Kim is a master of CMP process at Samsung Electronics and leads the CMP group in the foundry process development team. He holds BS and MS degrees in mechanical engineering from Seoul National University and joined Samsung in 1999 as a CMP engineer. With 25 years of CMP experience, he has provided various CMP solutions for DRAM, Flash and especially Logic process integration. His major contributions include the development of conditioning simulator, low-scratch CMP and CMP driven integration schemes for logic device. He is co-author of dozens of patents and papers related to CMP equipment, consumables and processes.