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Dr. ChangMin Park joined the process development team of Samsung Electronics' Semiconductor R&D center in 2000 after receiving a master's degree in electronic engineering from Korea University and participated in the development of next-generation DRAM, FLASH, and logic devices. He was mainly in charge of developing photolithography processes for next-generation product development, and has more than 14 years of experience, especially in EUV lithography development. He received a doctorate in electronics from Korea University in 2010 for his research on EUV patterning and flexible electronics.