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Mr. Sungwoo Ko received his MS degree in electrical engineering from the Seoul National University (Korea) in 2005. He has been developing OPC technology for DRAM, NAND, Logic, CIS and New Memory at SK hynix R&D since 2005. Now, He is the head of OPC Infrastructure Technology Team Leader at R&D. He is in charge of SK hynix OPC Technology Roadmap Establishment, Advanced OPC Technology Development, Machine learning based OPC Application Development.