



Prof. Hyungtak Seo

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Prof. H. Seo received a doctoral degree in 2008 from North Carolina State University and worked as a postdoctoral researcher in Lawrence Berkeley National Laboratory until September 2011. He has served as a professor of Ajou University since 2011 till now. He is PI of Advanced Electronic & Energy Materials laboratory (AEEM) in Ajou University. His current interests are in (i) fabrication of nanomaterials for solar cells and photocatalysis applications; (ii) design of materials/devices for next generation ICs (resistive memory, logic) and neuromorphic devices; (iii) tuning nanoscale semiconductor electronic properties for electrical and energy applications; (iv) and analysis for electronic properties of semiconductors and interfaces via UPS, XPS, ellipsometry, etc. His research is recently strongly focused on active and passive application of metal oxides to ICs including high-k dielectrics for MOSFET gates and MIM capacitors and channel for redox FETs, Mott MIT FETs in PIM and neuromorphic transistors.