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Hyung-Seok Lee received the B.S. and M.S. degrees in electrical engineering from Korea University, Seoul, Korea, in 2000 and 2002, respectively, and the Tech. Lic. And Ph.D. degrees from the Royal Institute of Technology (KTH), Stockholm, Sweden, in 2005 and 2008, respectively. From 2001 to 2002, he was a Researcher with Samsung SDI Central R&D Center, Suwon, Korea. From 2007 to 2008, he was a Researcher with TranSiC AB, Sweden. From 2008 to 2013, he was a Postdoc in Massachusetts Institute of Technology (MIT)-Microsystems Technology Laboratories (MTL), Cambridge, MA, US. In 2013, he was a Senior Researcher with Samsung Corning R&D Center, Asan, Korea. Since 2014, he has been a Chief Technical Staff with Electronics and Telecommunications Research Institute (ETRI), Daejeon, Korea. His research interests include the process technology and device design of high-power devices in wide-bandgap materials. He contributed more than 60 publications including international journals and conference proceedings.